

ABSTRACT OF THE DISCLOSURE

It is an object to provide a technique capable of reducing a change in a design which is required for varying a cache capacity. A plurality of cache memories (6) are connected to a bank control circuit (7). The bank control circuit (7) selects at least one
5 of the cache memories (6) based on a signal (CSIZE) indicative of a cache capacity and permits to the selected cache memory (6) to be accessed by a bus control circuit (3). Accordingly, it is possible to vary the cache capacity by changing a value of the signal (CSIZE).